

FIG. 1

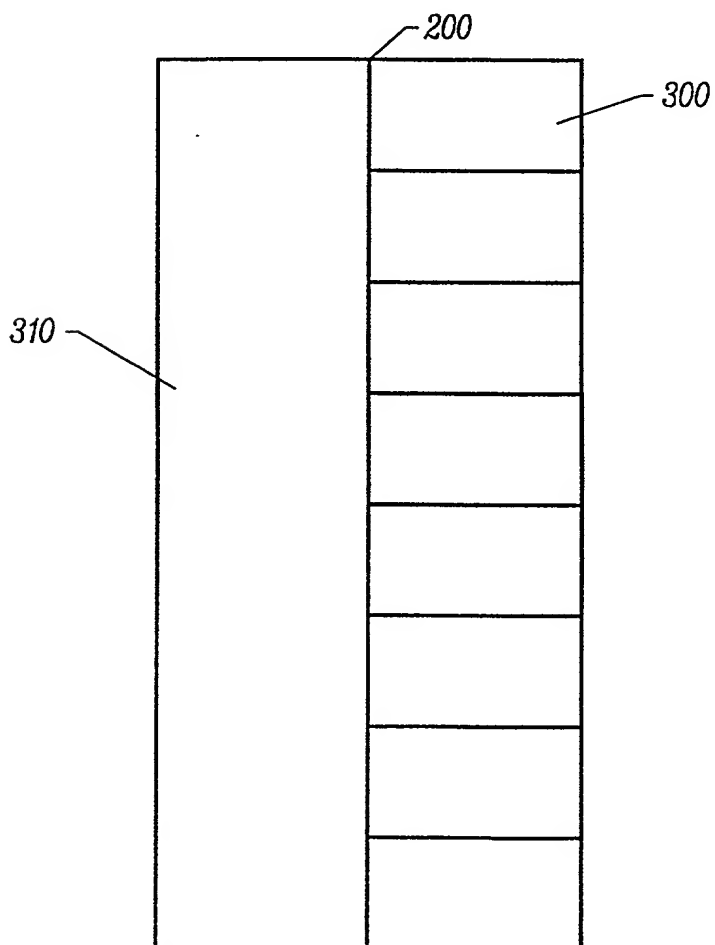


FIG. 3

200 210 220 225 230 121

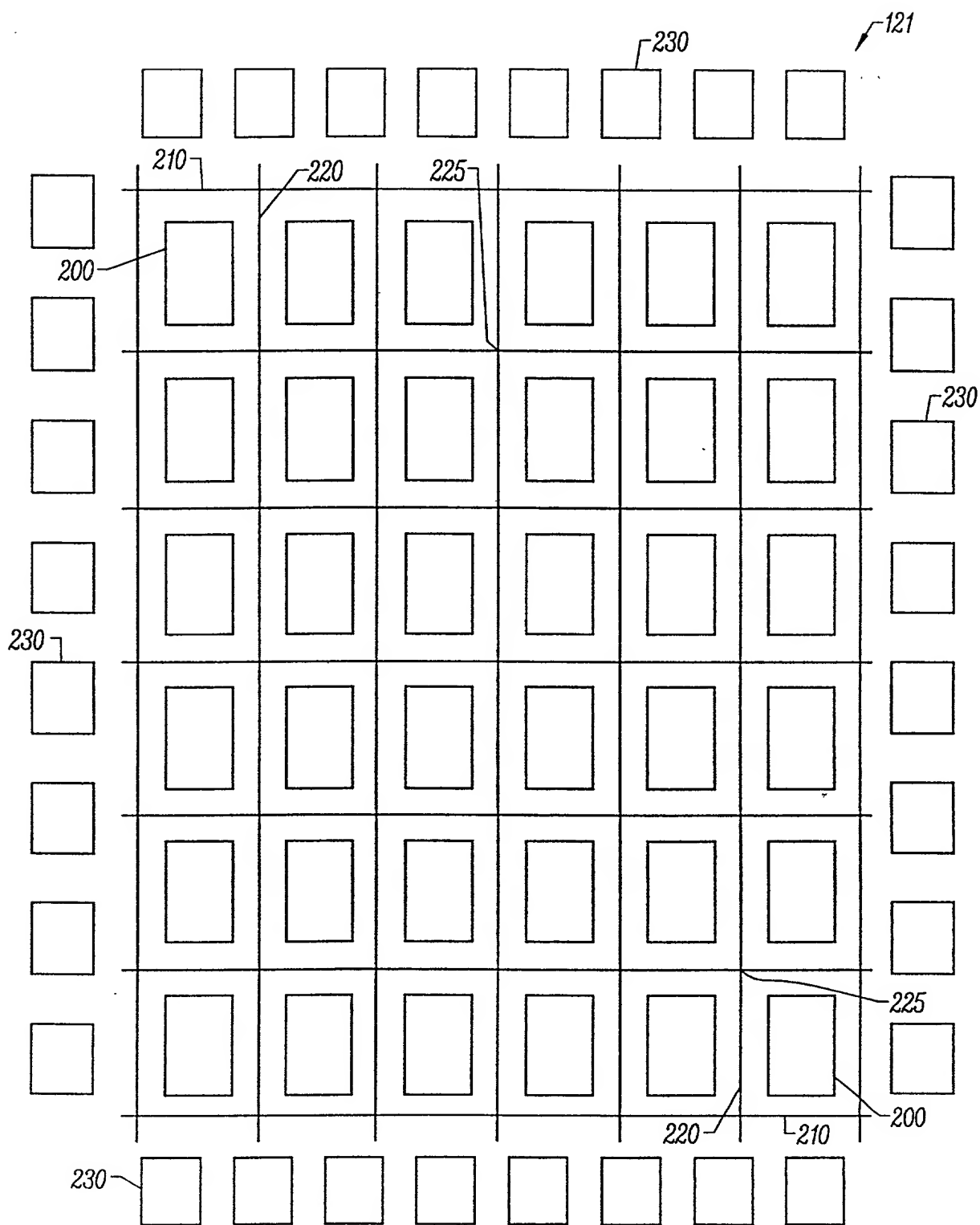


FIG. 2

400

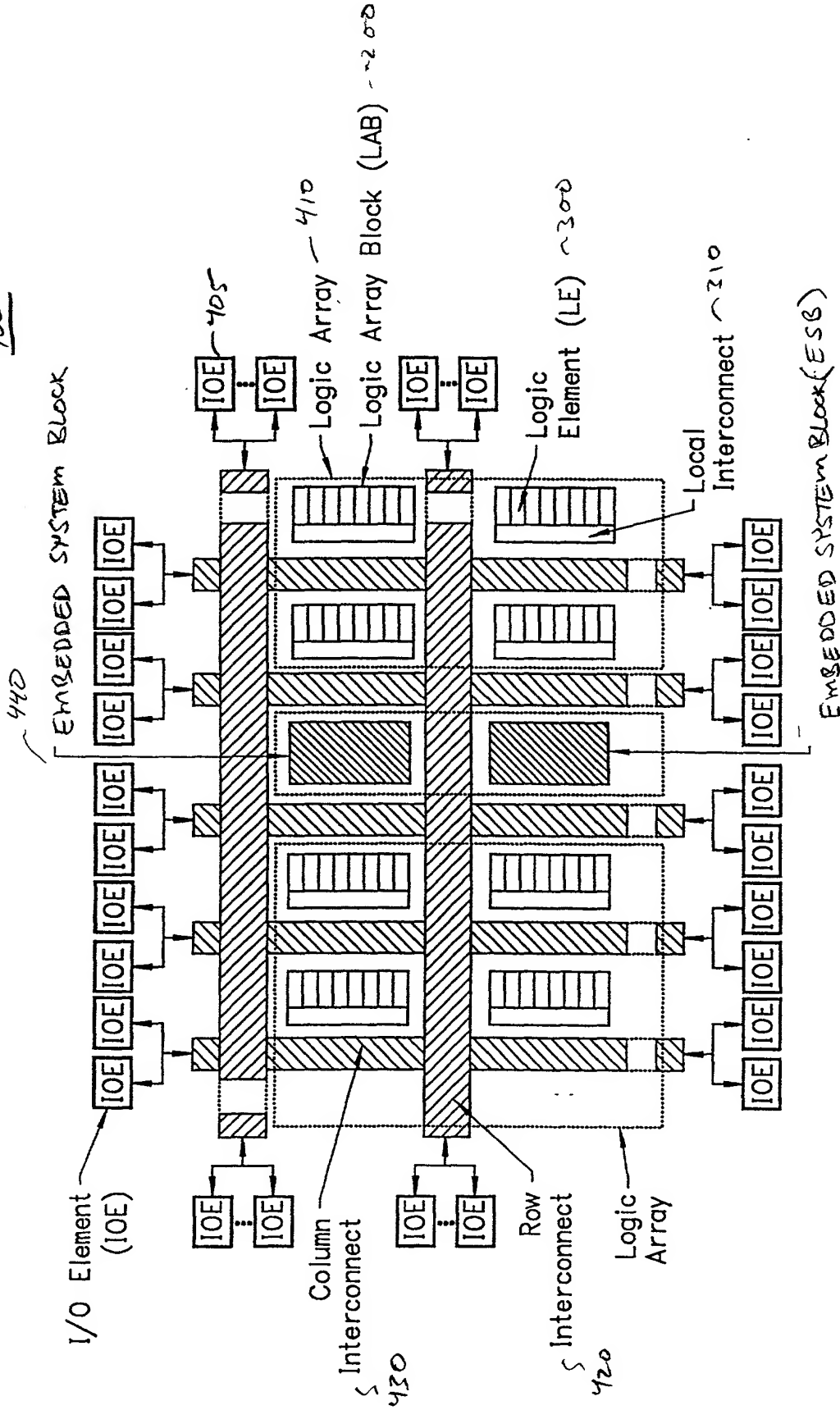


FIG. 4

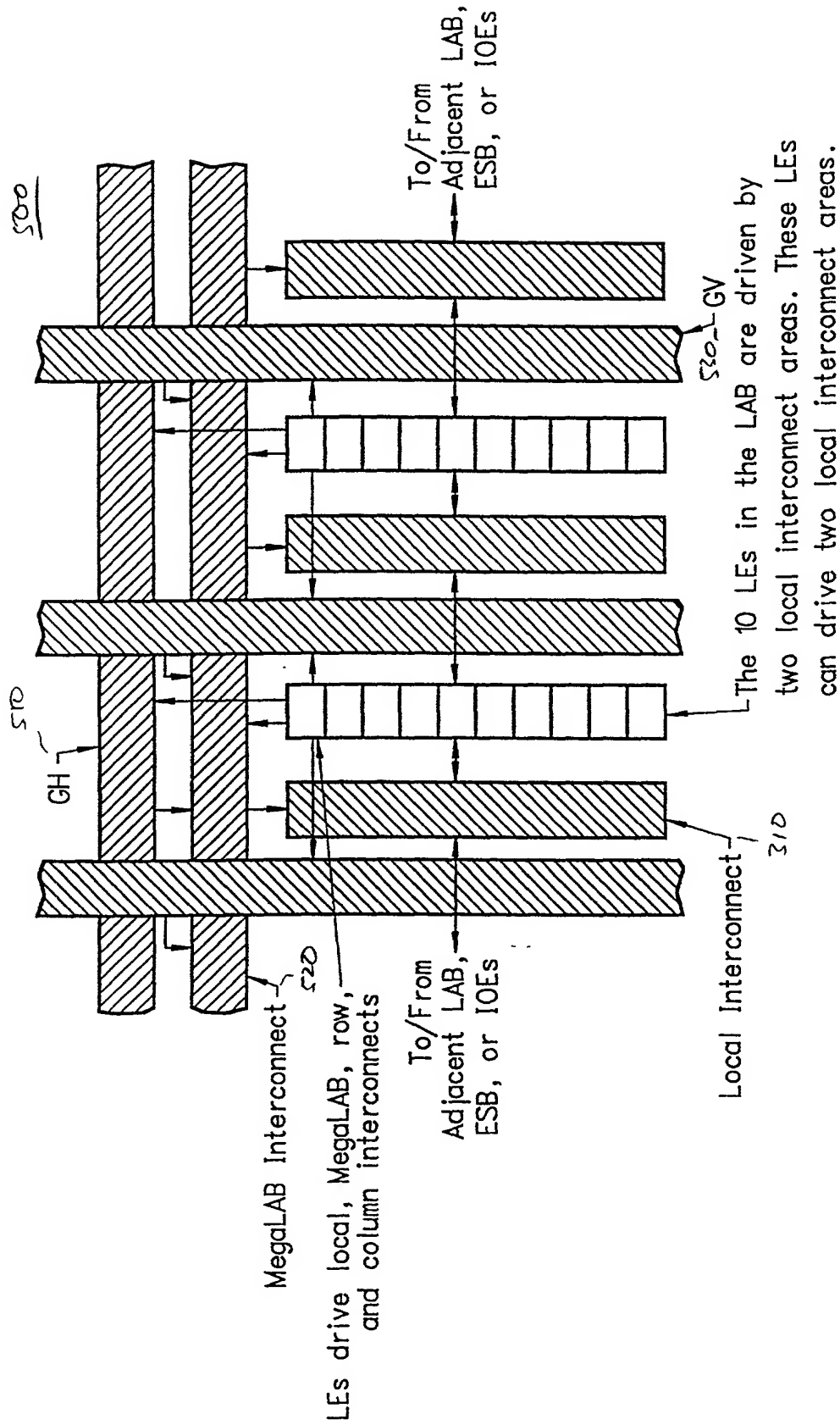


FIG. 5

600

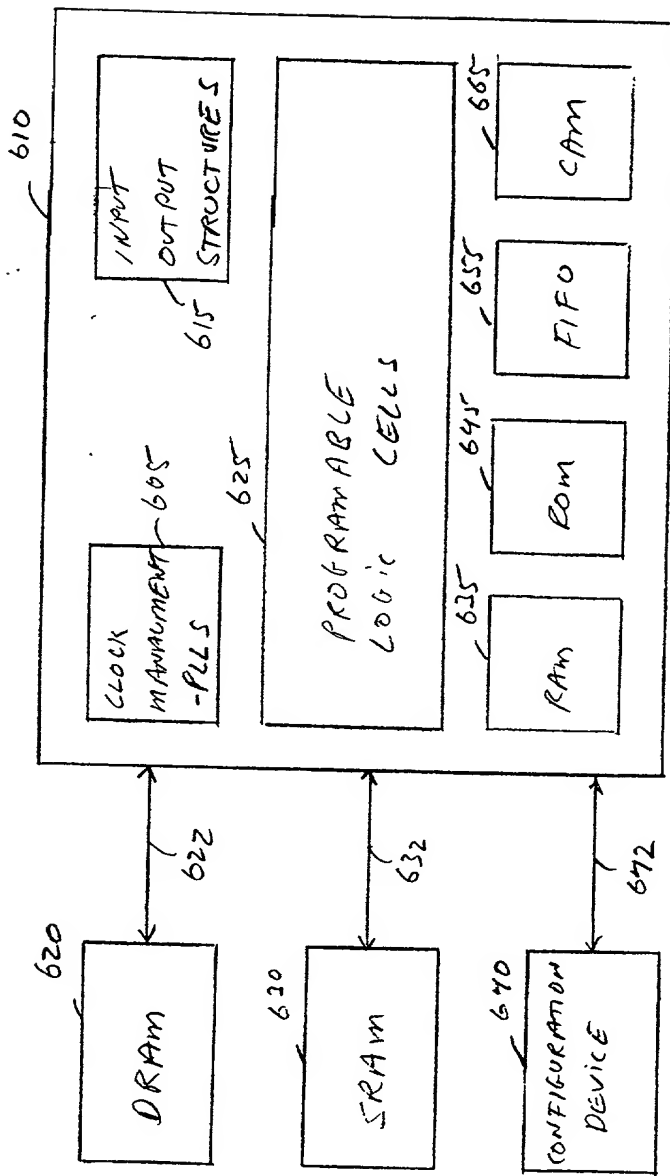


FIGURE 6

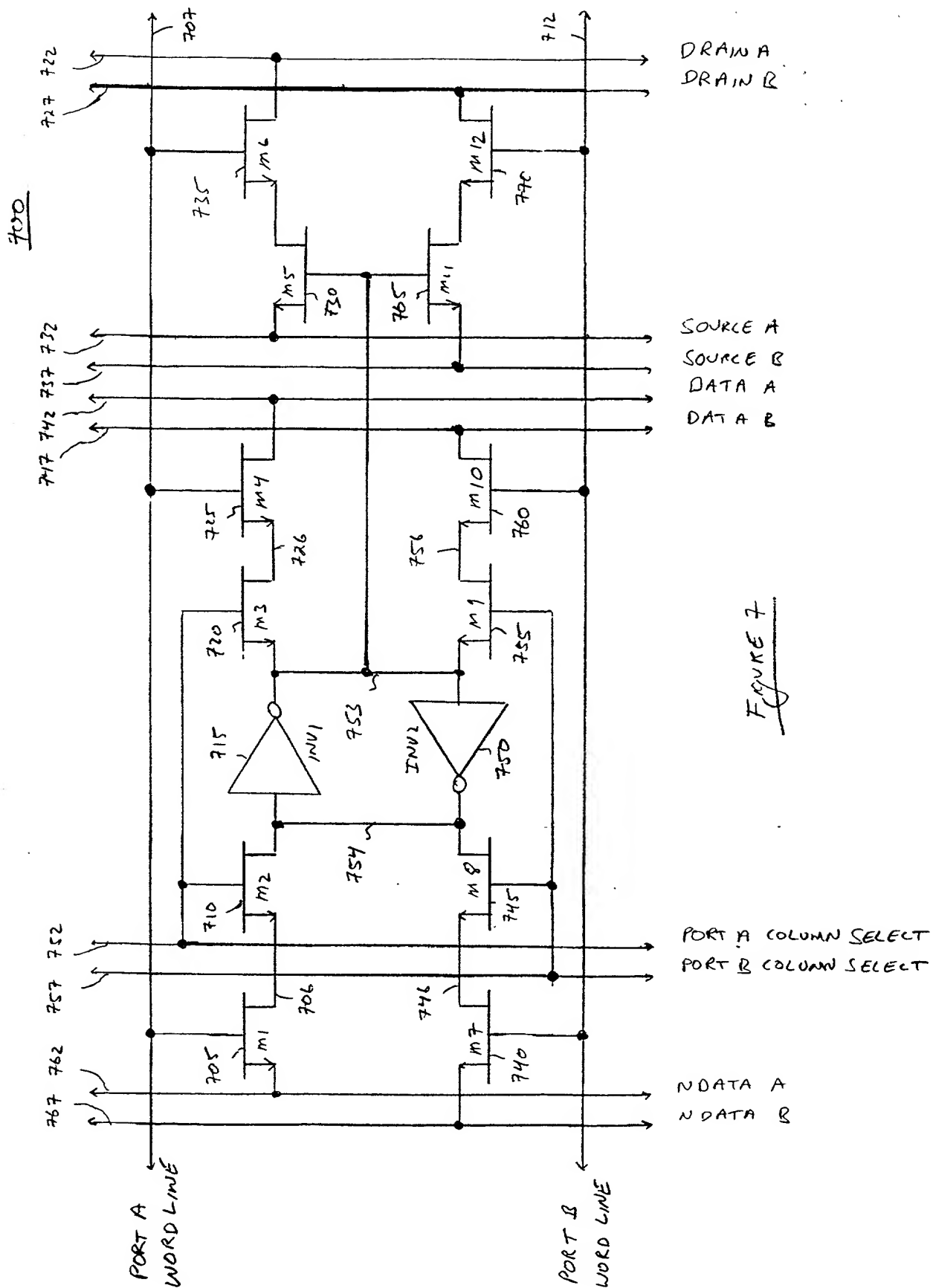


FIGURE 7

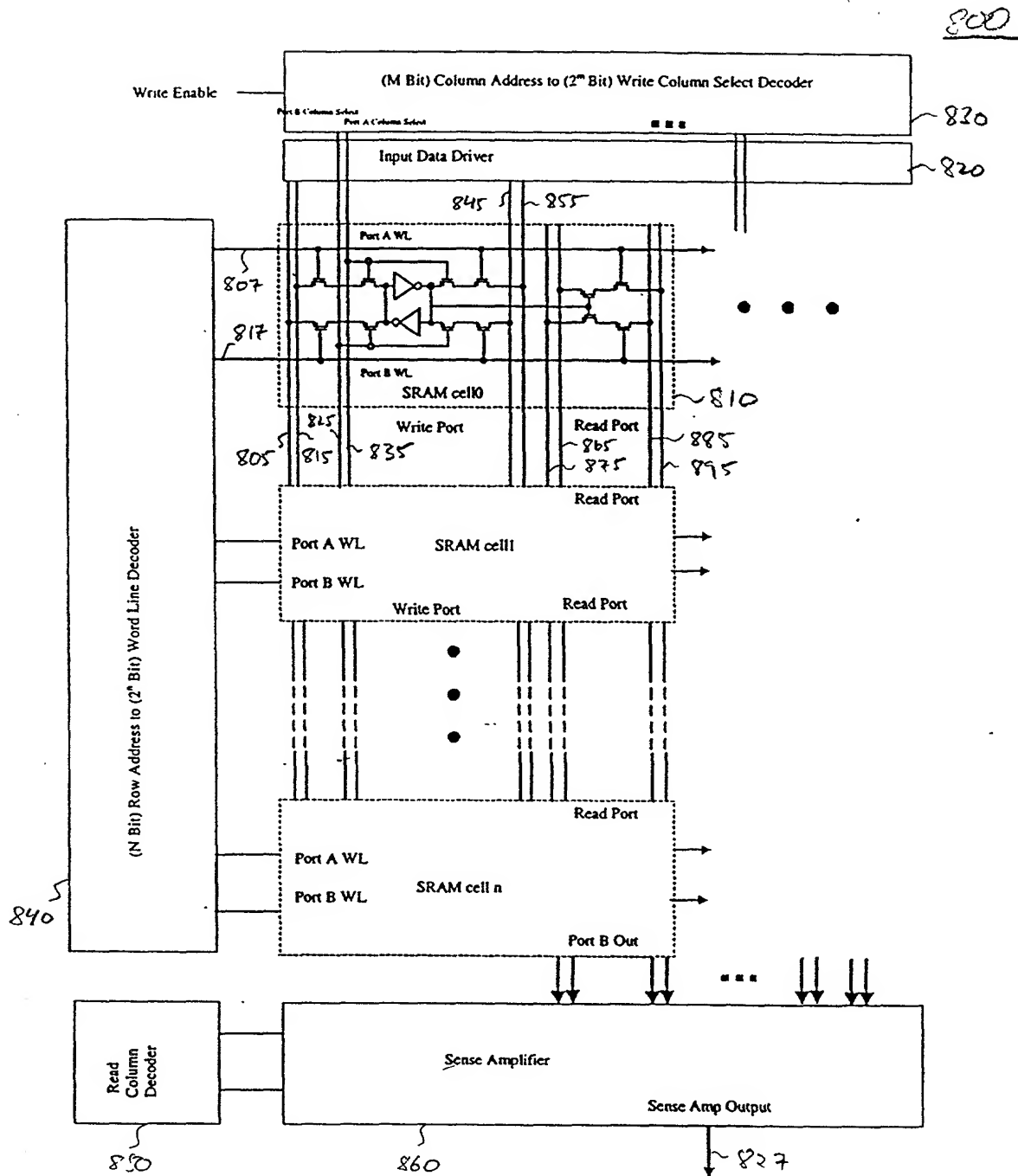


FIGURE 8

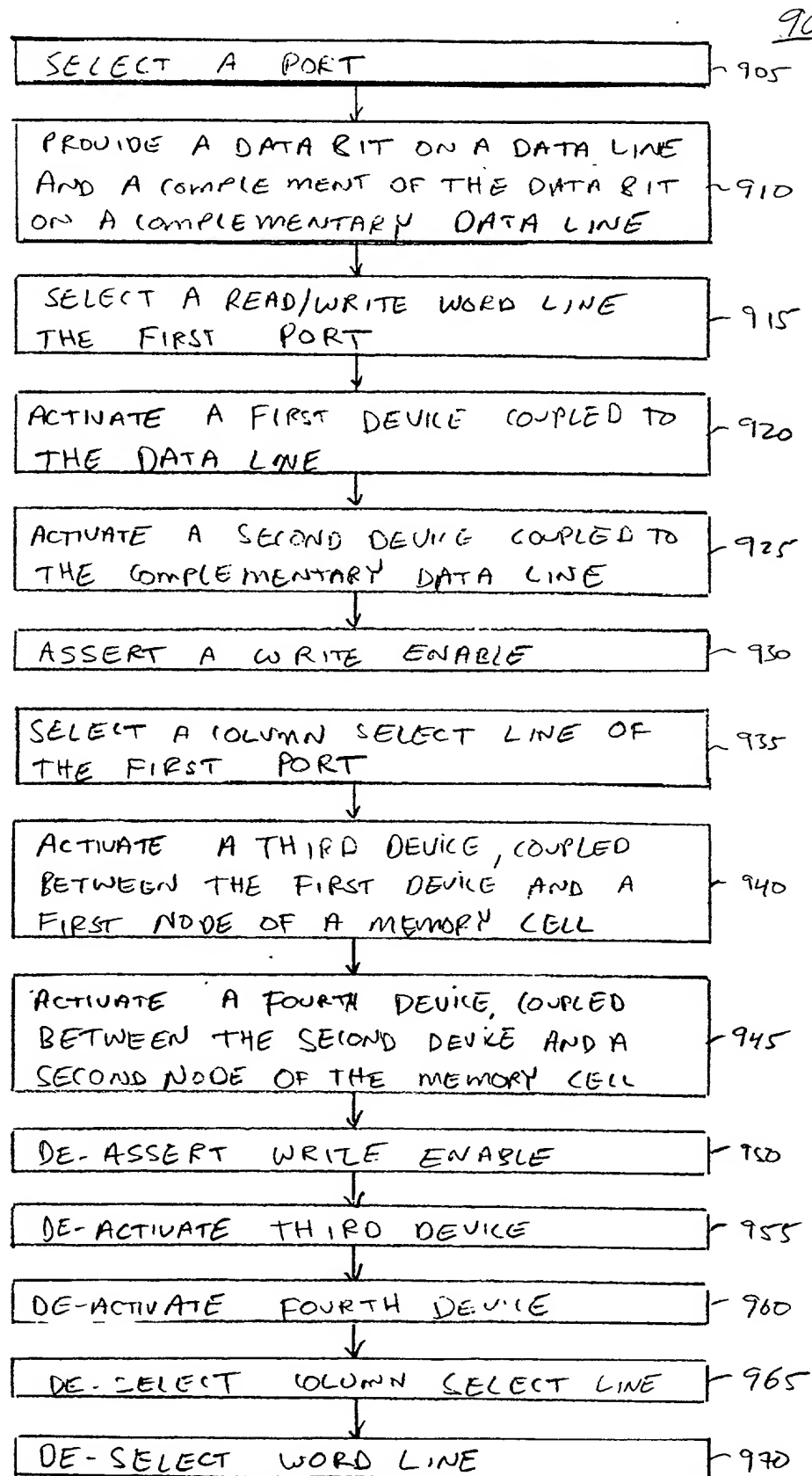


FIGURE 9



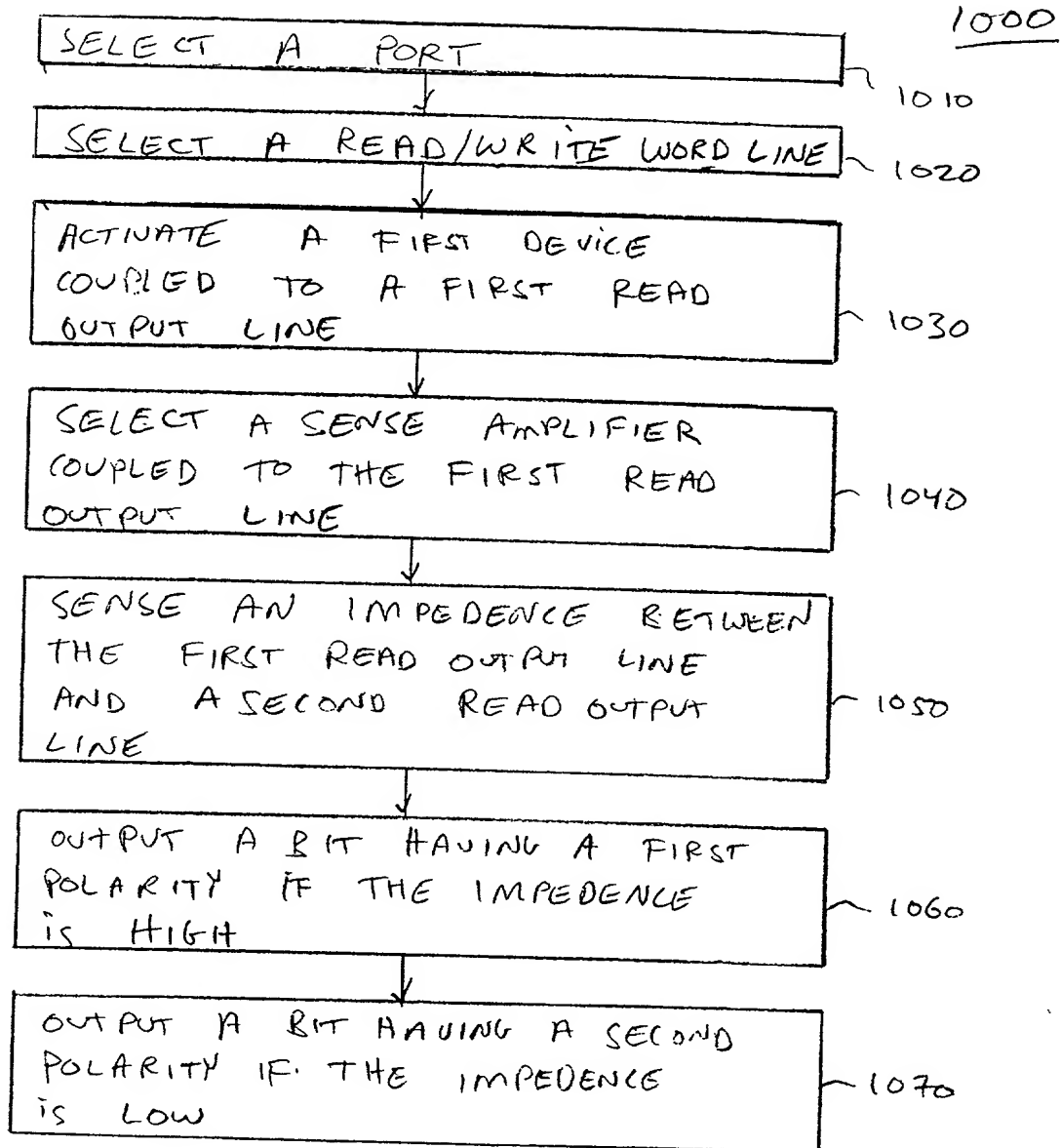
[illegible]

FIGURE 10

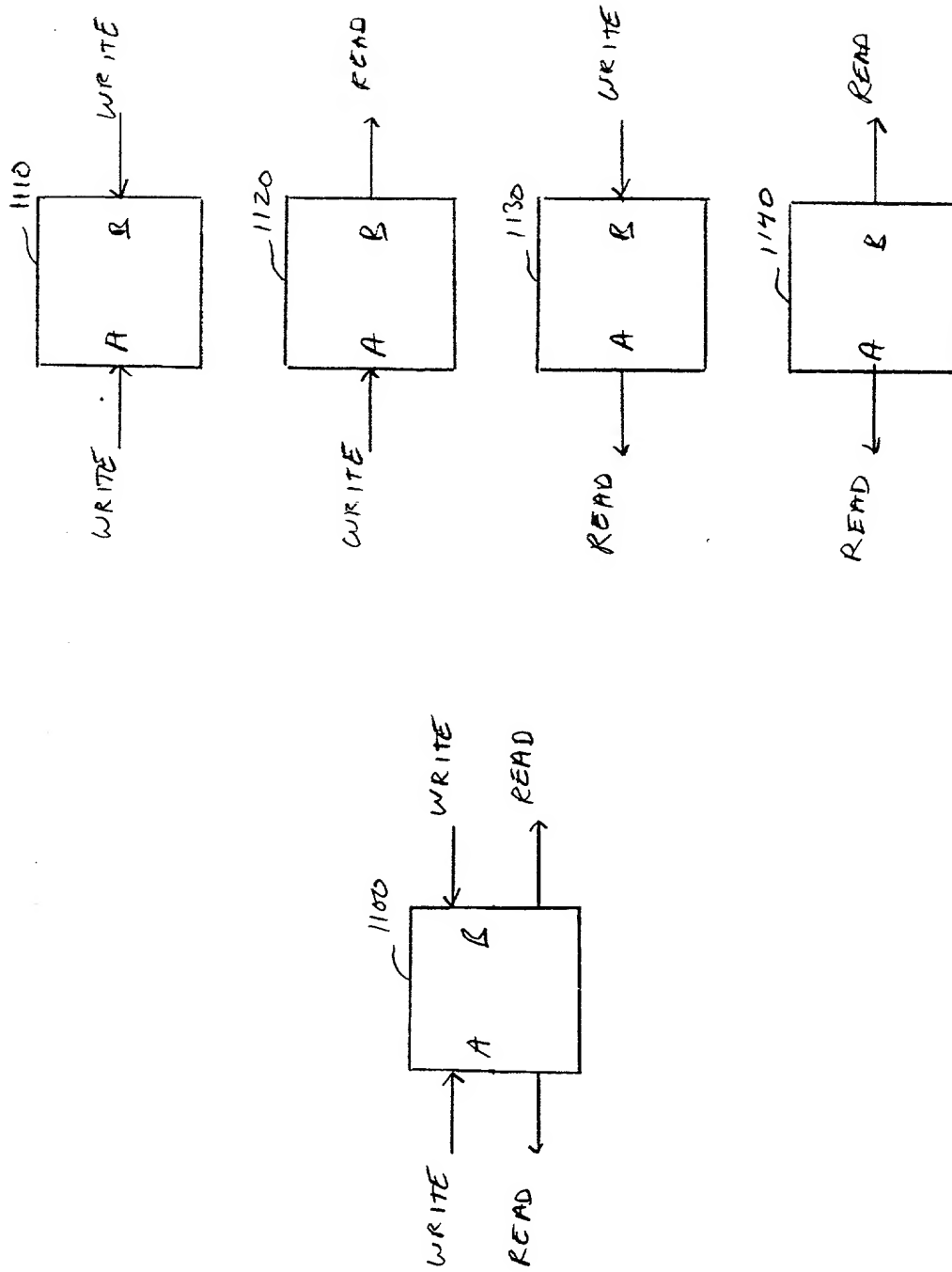


FIGURE 11

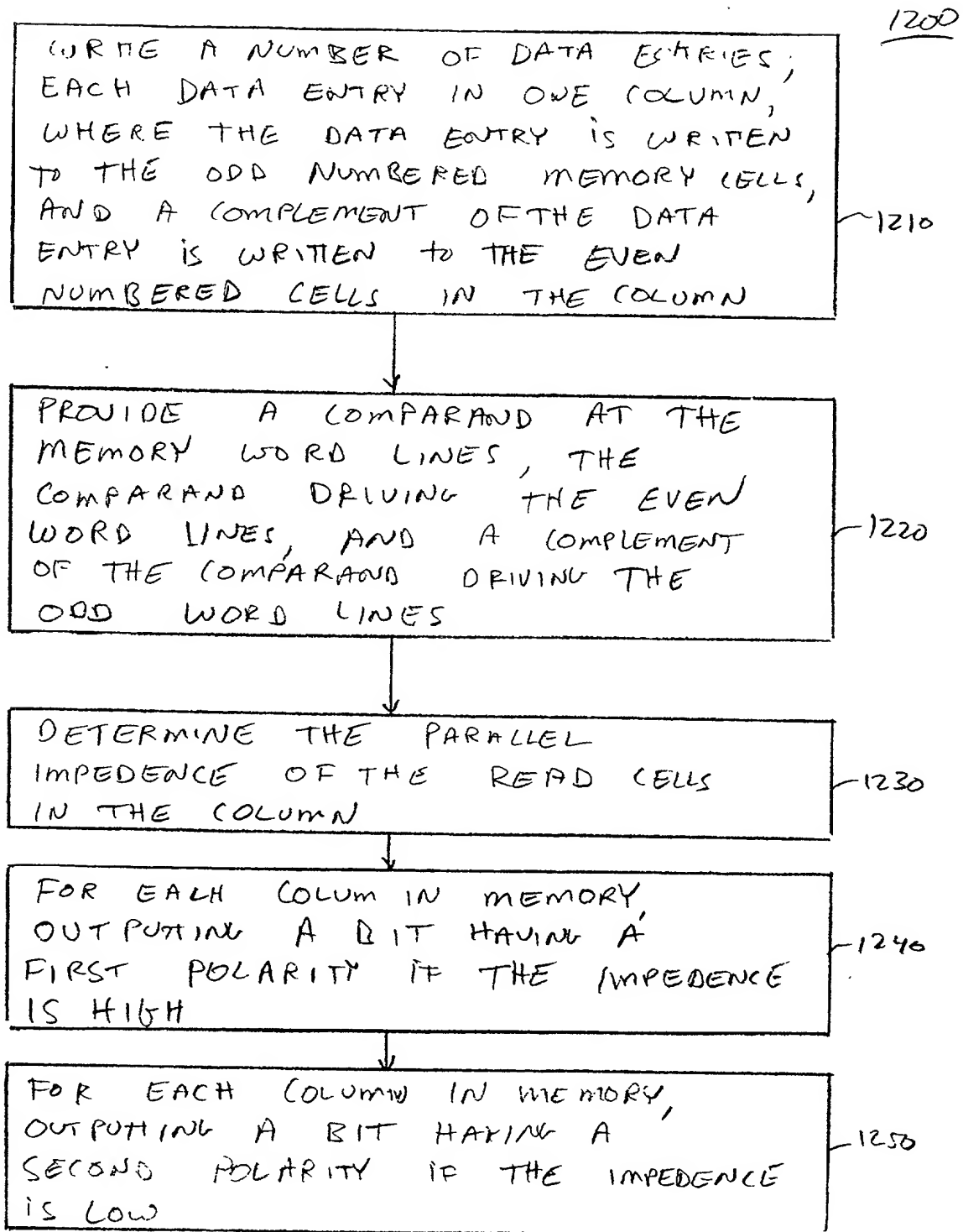
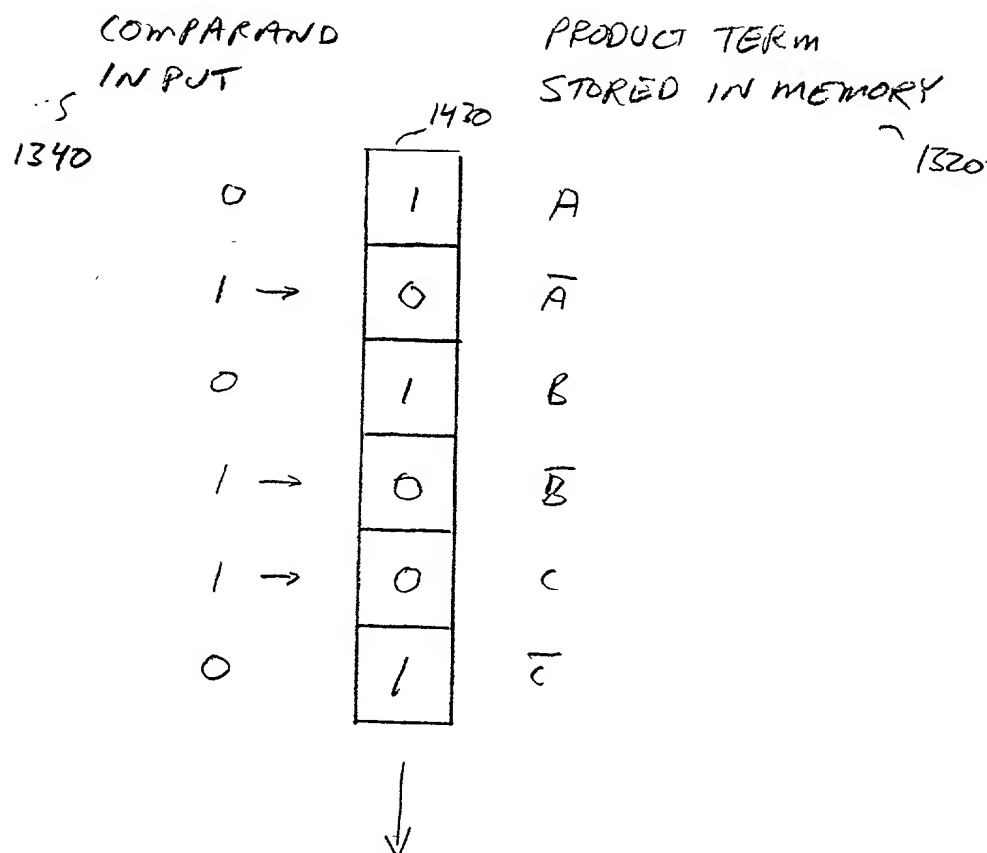


Figure 12

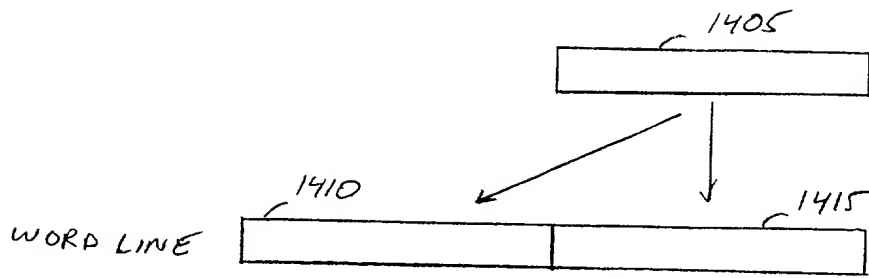
$$\text{PRODUCT TERM} = A \cdot B \cdot \bar{C}$$

A "MATCH" OCCURS WHEN  $A=1, B=1, C=0$



HIGH IMPEDENCE AT SENSE AMPLIFIER INPUT

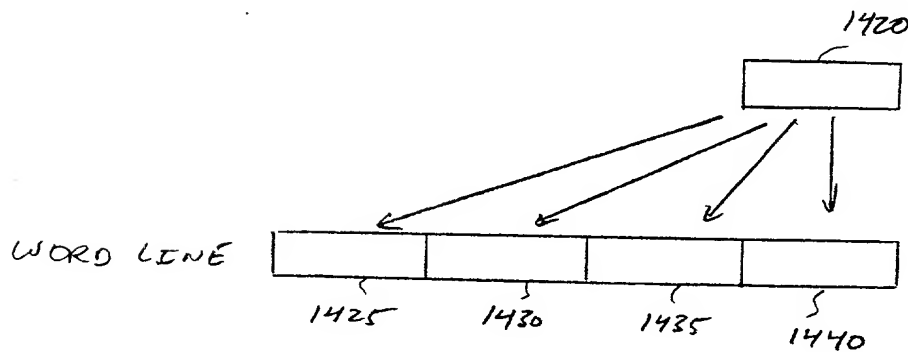
FIGURE 13



DATA TO  
BE WRITTEN

1:2  
ONE EXTRA  
ADDRESS BIT

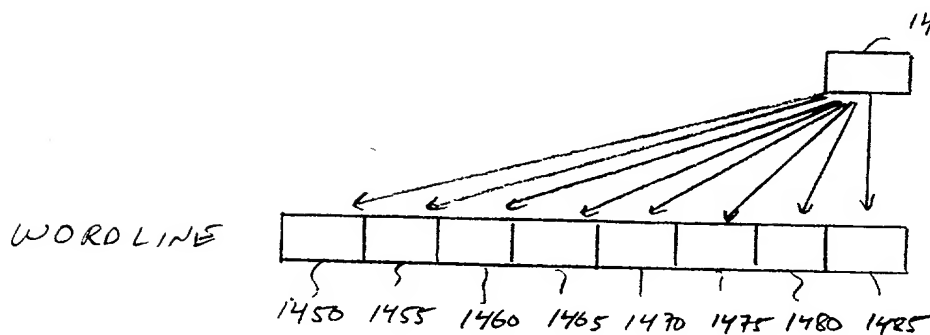
Figure 14A



DATA TO  
BE WRITTEN

1:4  
TWO EXTRA  
ADDRESS BITS

Figure 14B



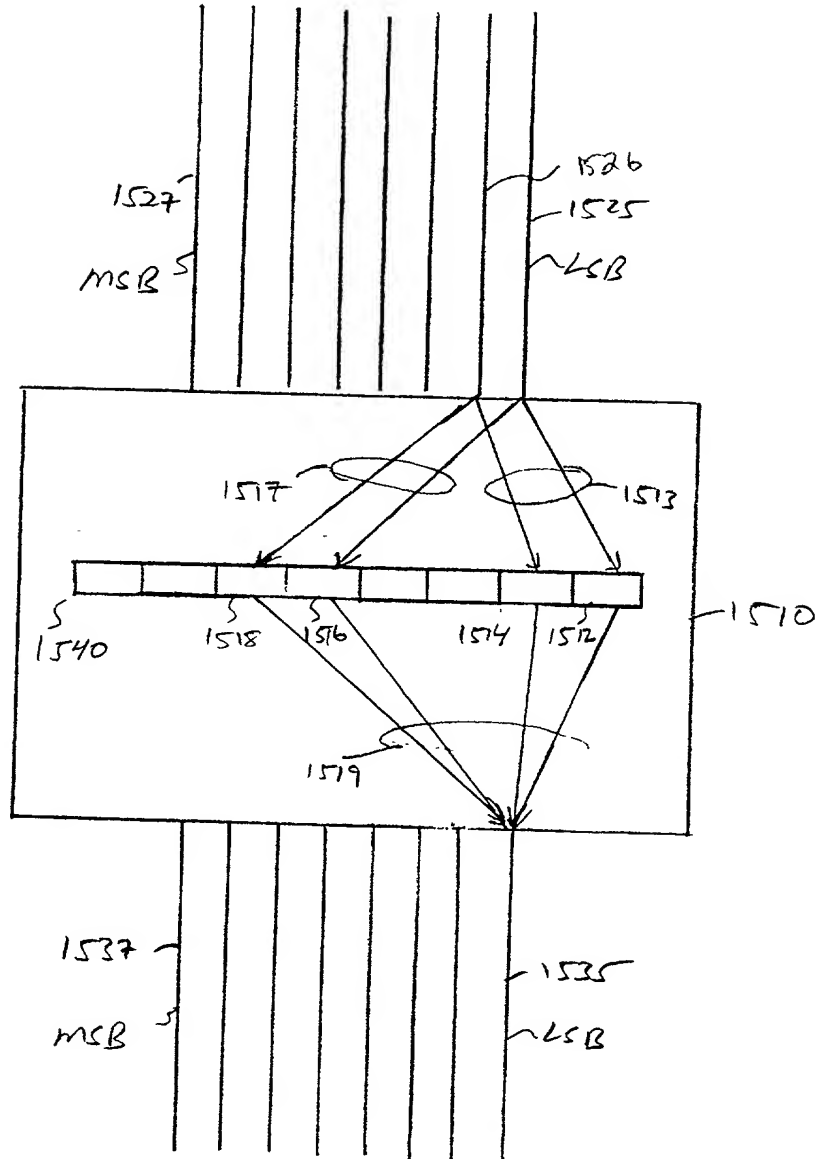
DATA TO  
BE WRITTEN

1:8  
THREE EXTRA  
ADDRESS BITS

Figure 14C

INPUT DATA BUS - 1520

1500



OUTPUT DATA BUS - 1530

FIGURE 15

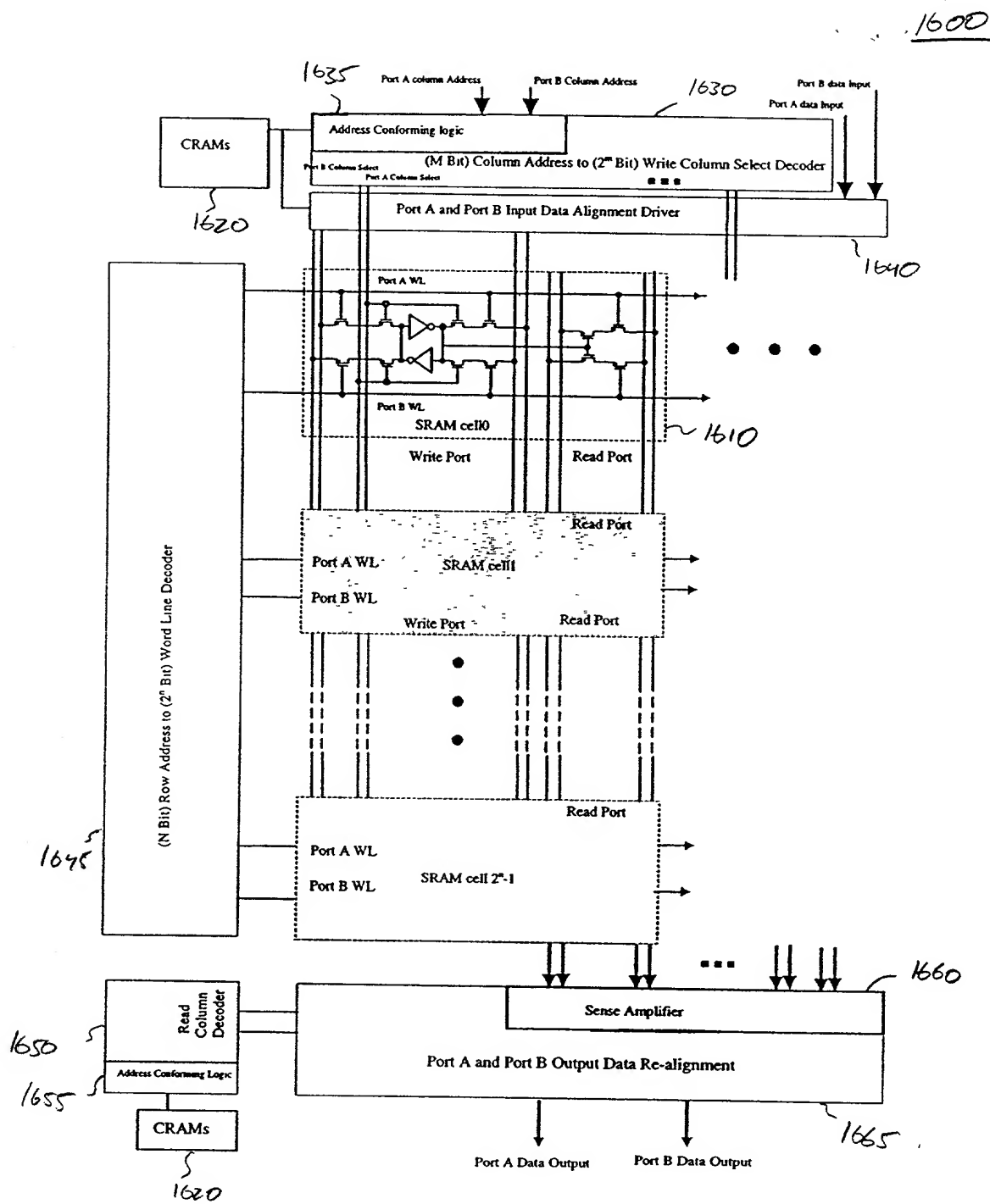


FIGURE 16

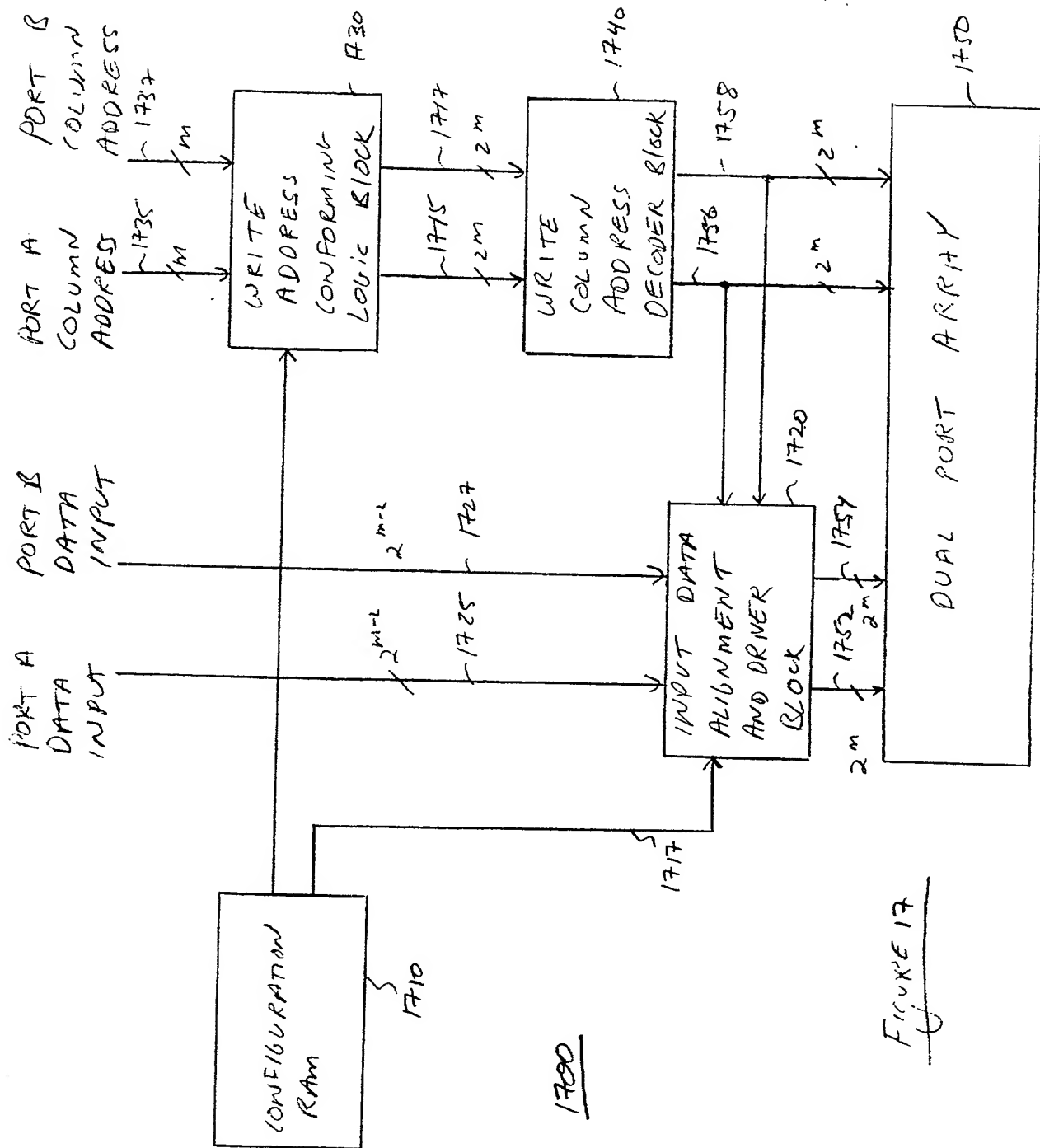


FIGURE 17



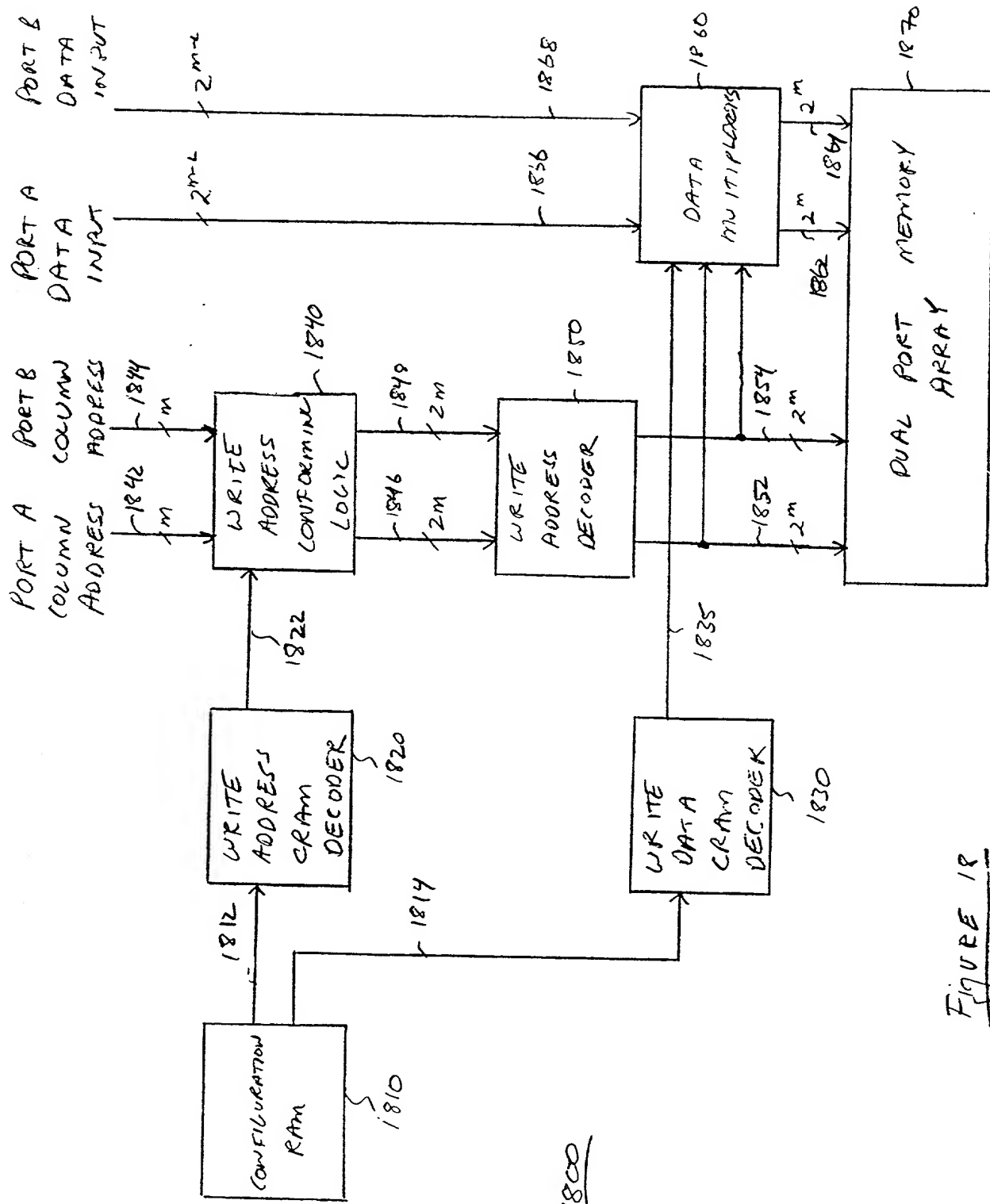


FIGURE 18

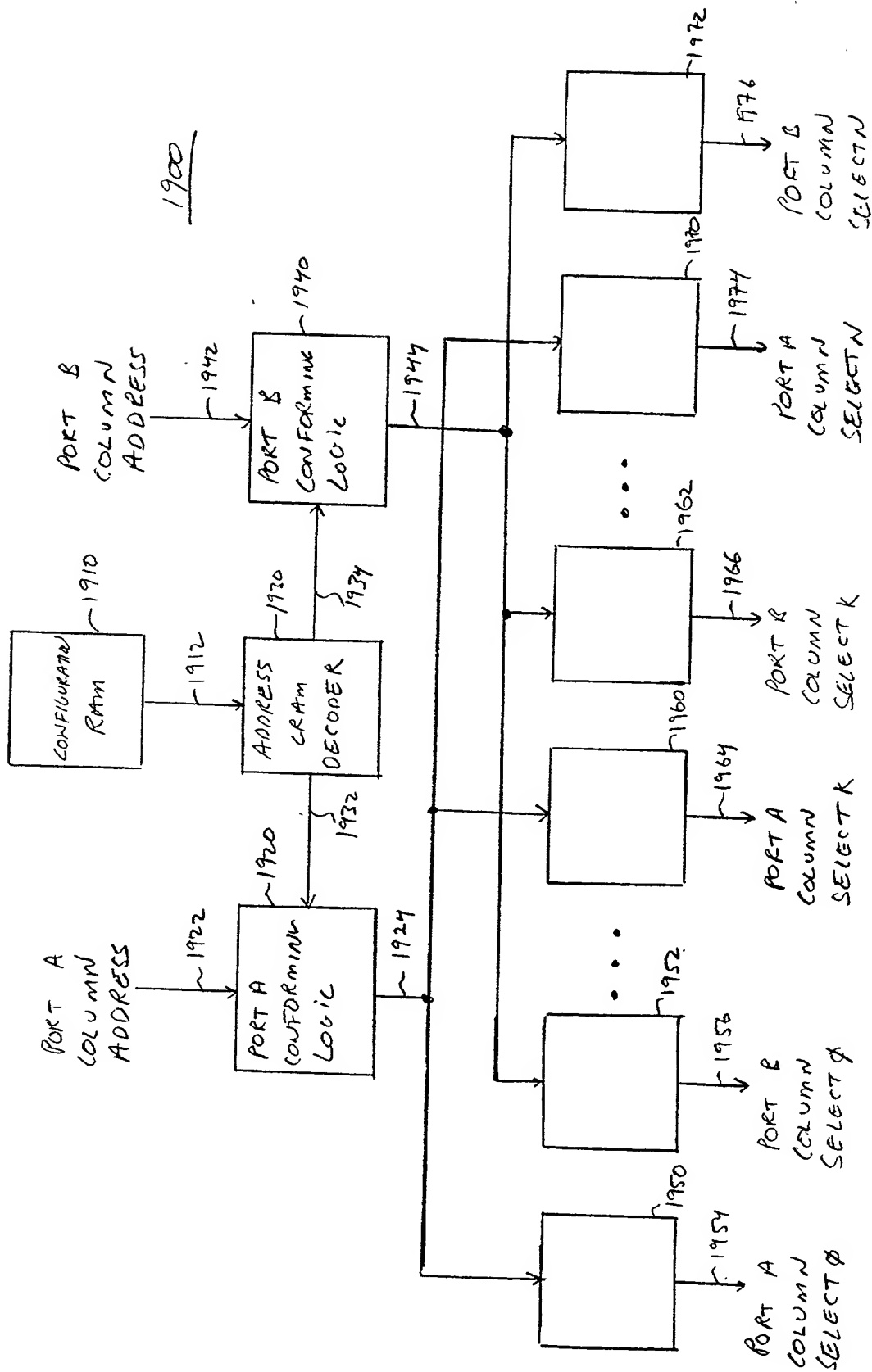


FIGURE 19

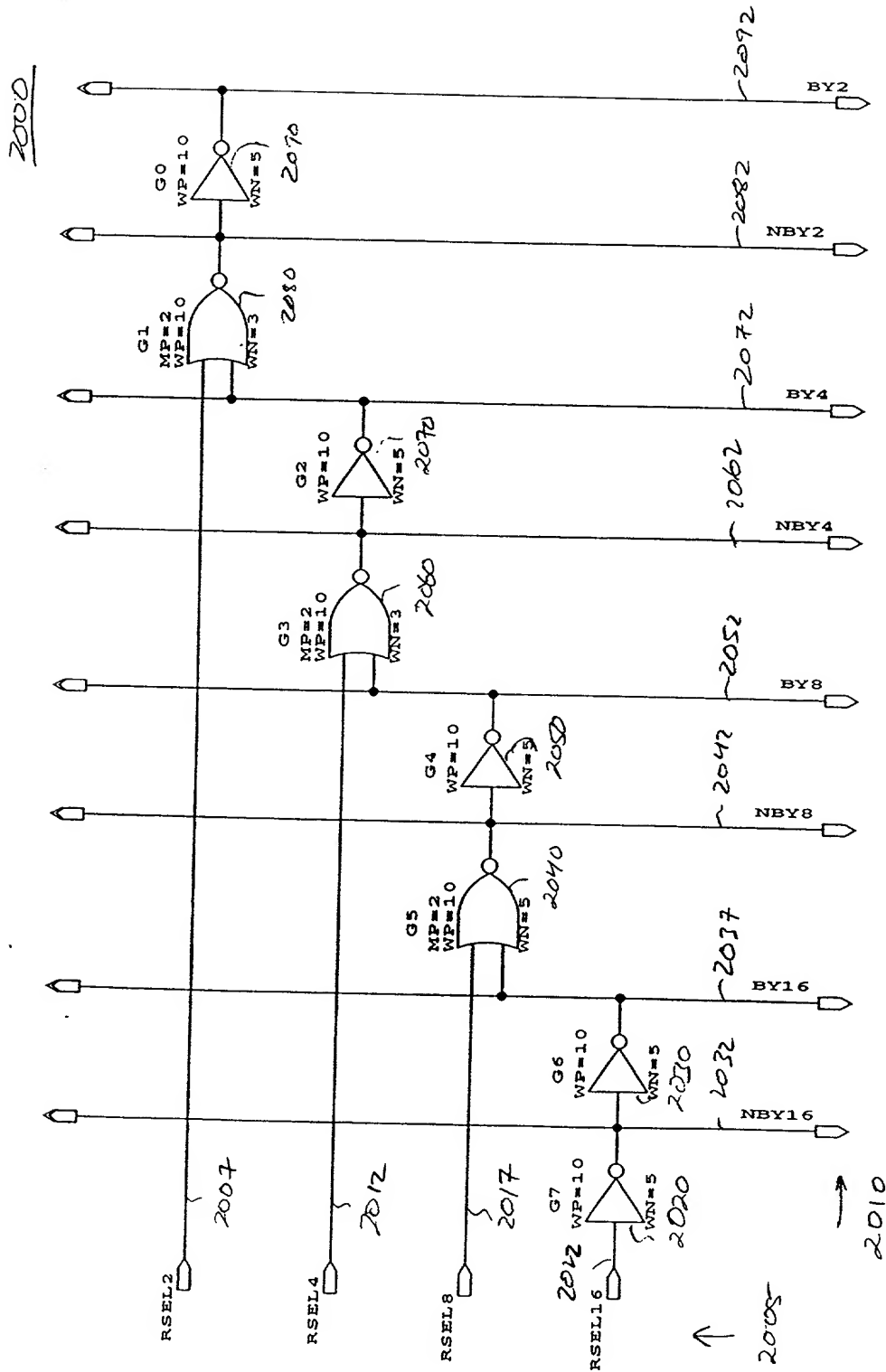


Figure 20

202006050000

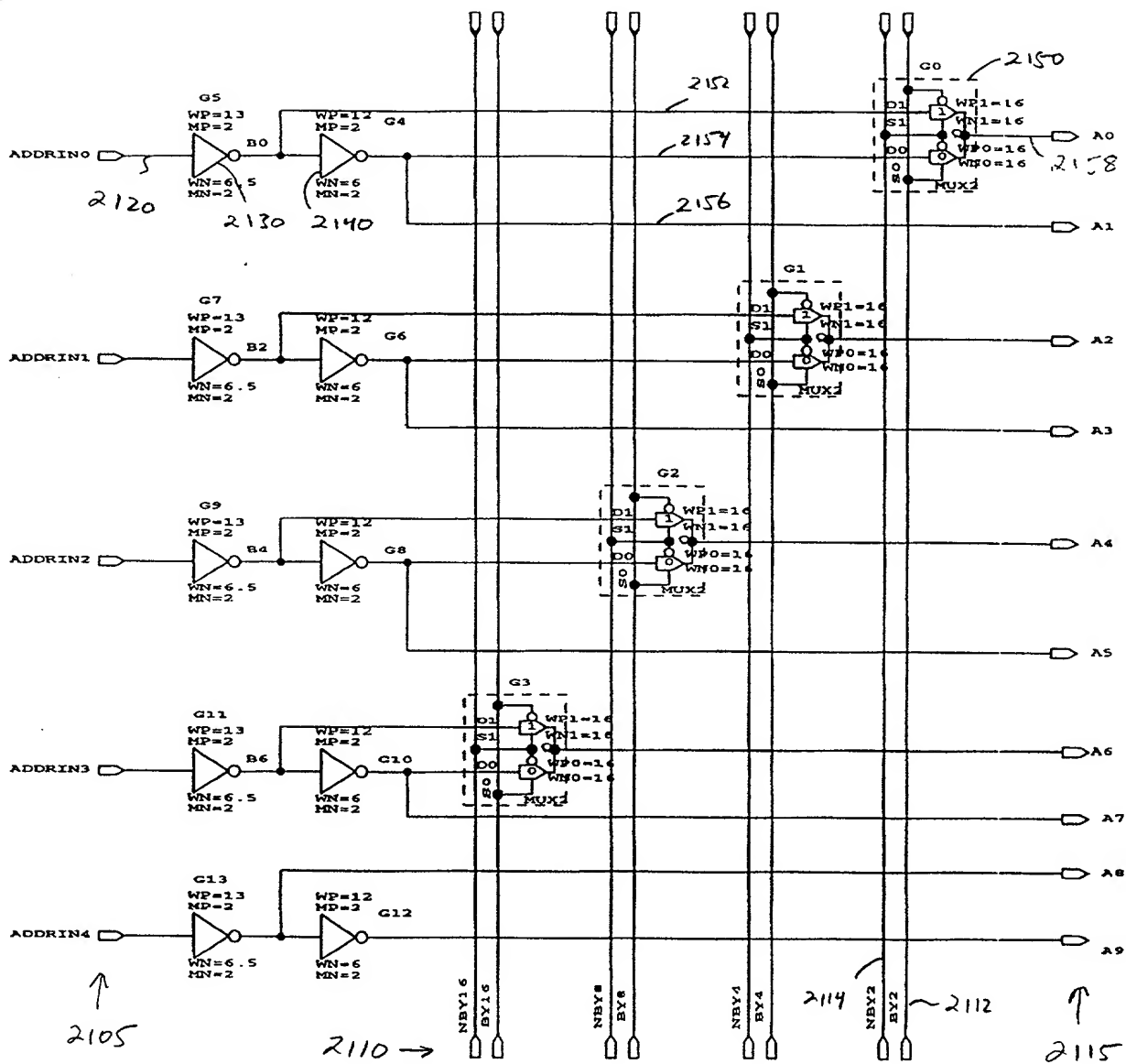


FIGURE 21

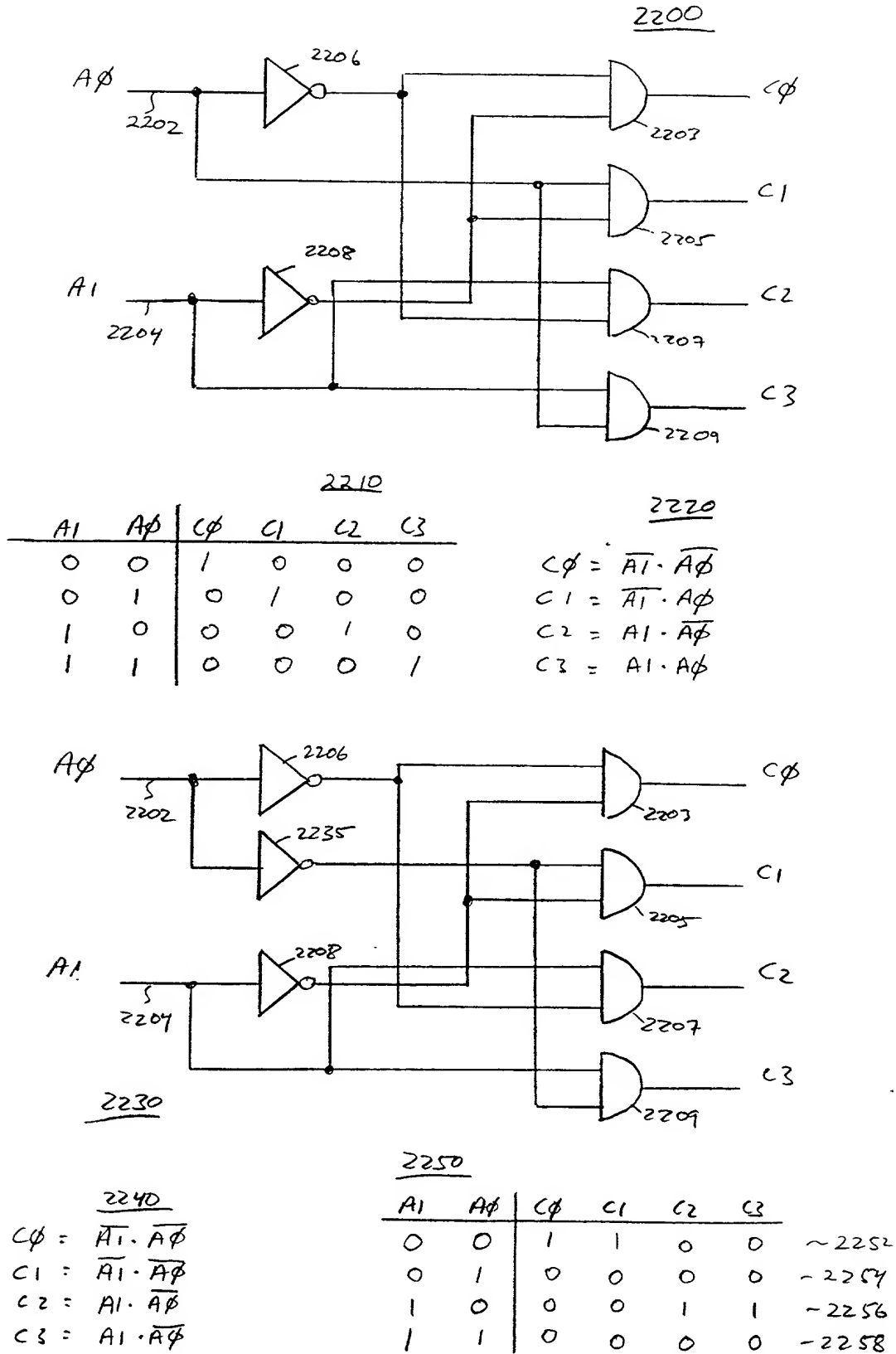


FIGURE 22

2315 2320 2330 2335

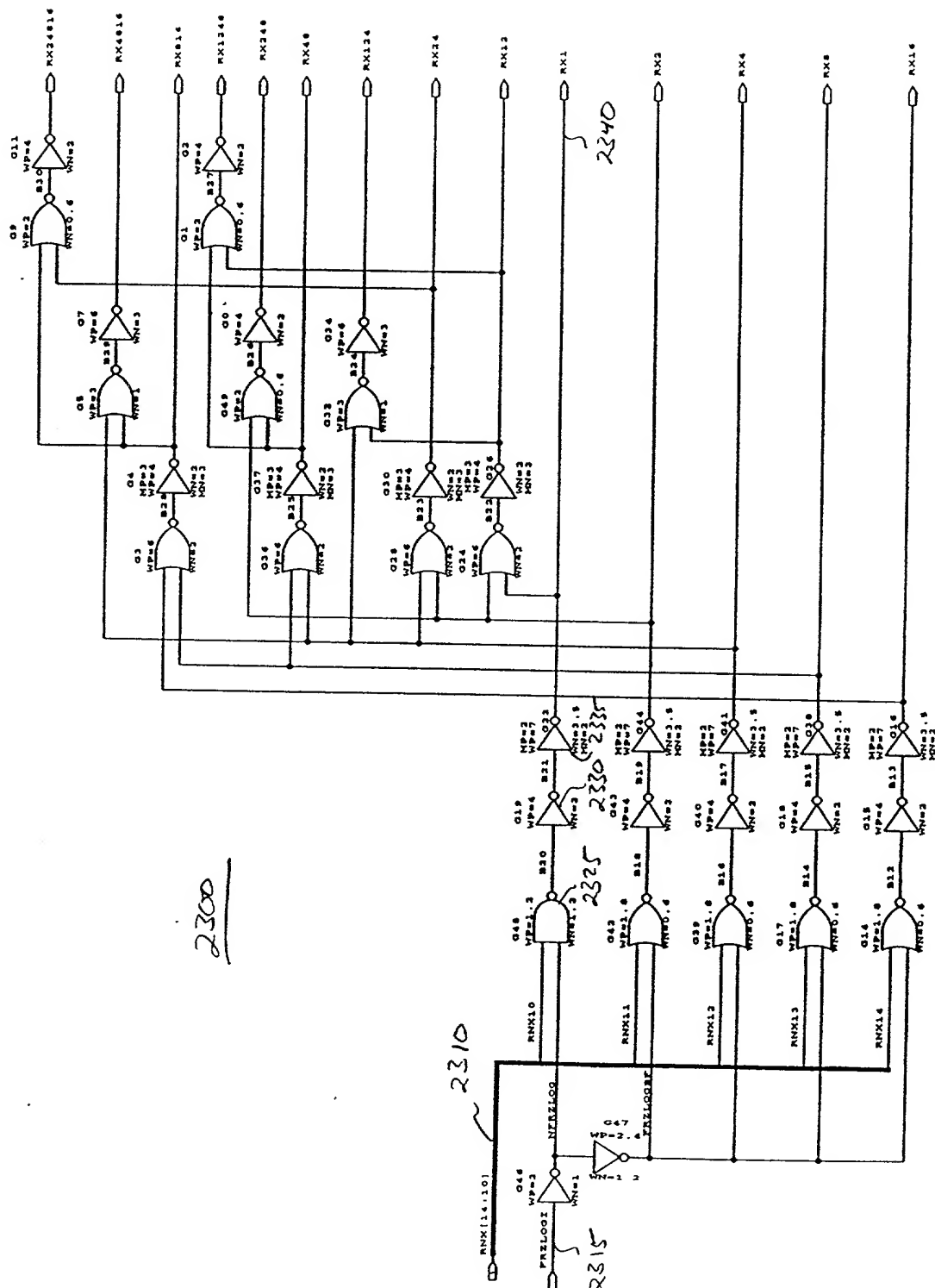
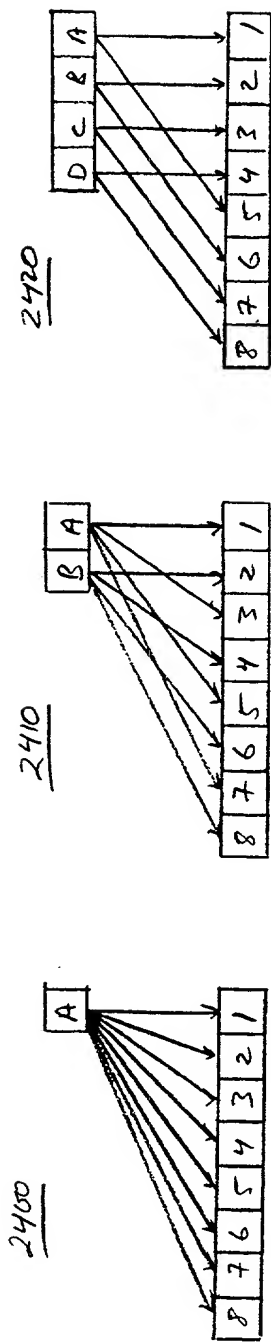


Figure 23



2432 ~ 2434 #bits/word

MUX	1	2	4	INPUTS	TYPE MUX
1	A	A	A	A	1:1
2	A	B	B	A, B	2:1
3	A	A	C	A, C	2:1
4	A	B	D	A, B, D	3:1
5	A	A	A	A	1:1
6	A	B	B	A, B	2:1
7	A	A	C	A, C	2:1
8	A	B	D	A, B, D	3:1

2442 ~ 2444

MUX	OUTPUTS	TYPE MUX
A	8, 7, 6, 5, 4, 3, 2, 1	1:8
B	8, 6, 4, 2	1:4
C	7, 3	1:2
D	8	1:1

Figure 24

2500 663400

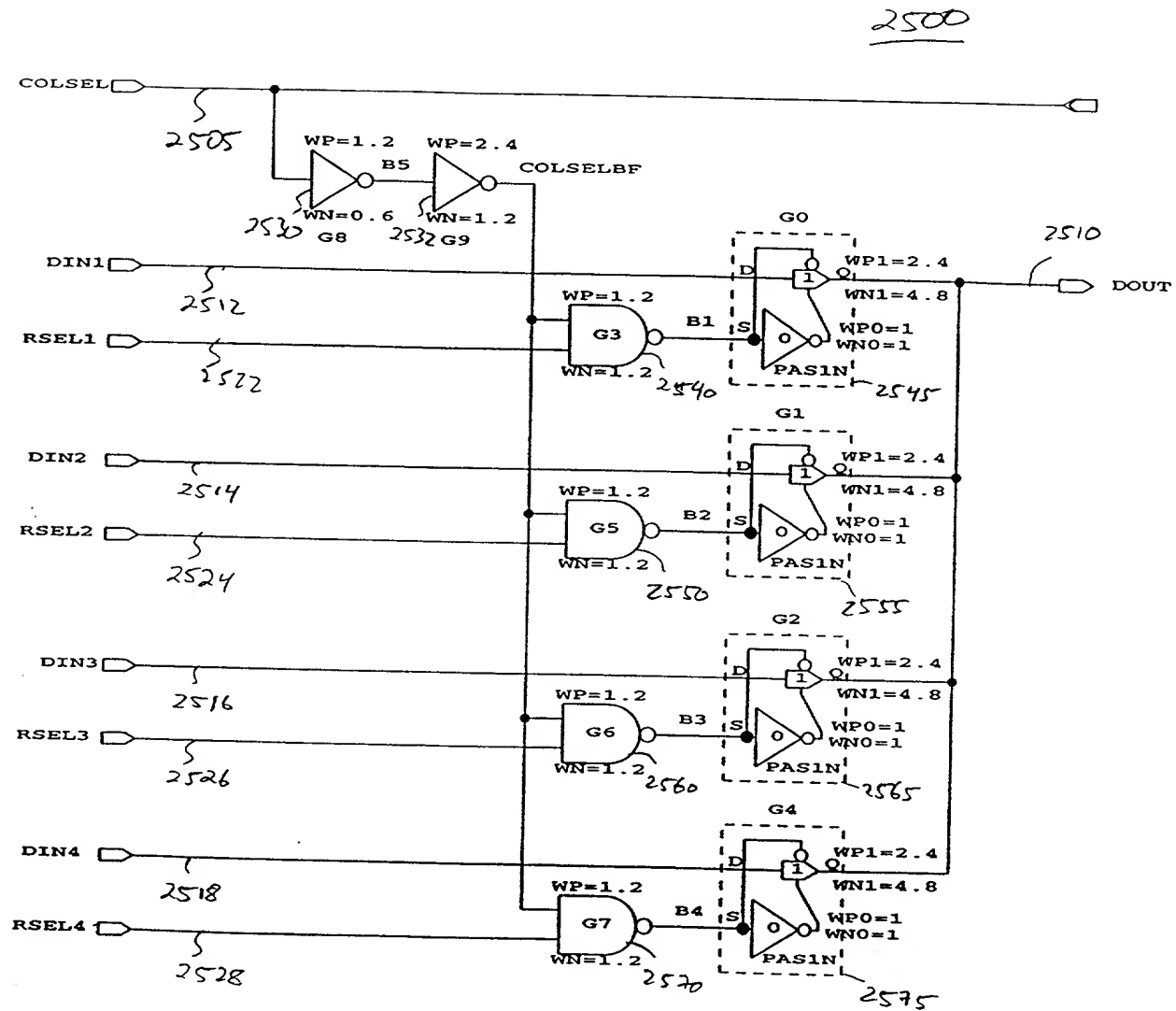


Figure 25



2600

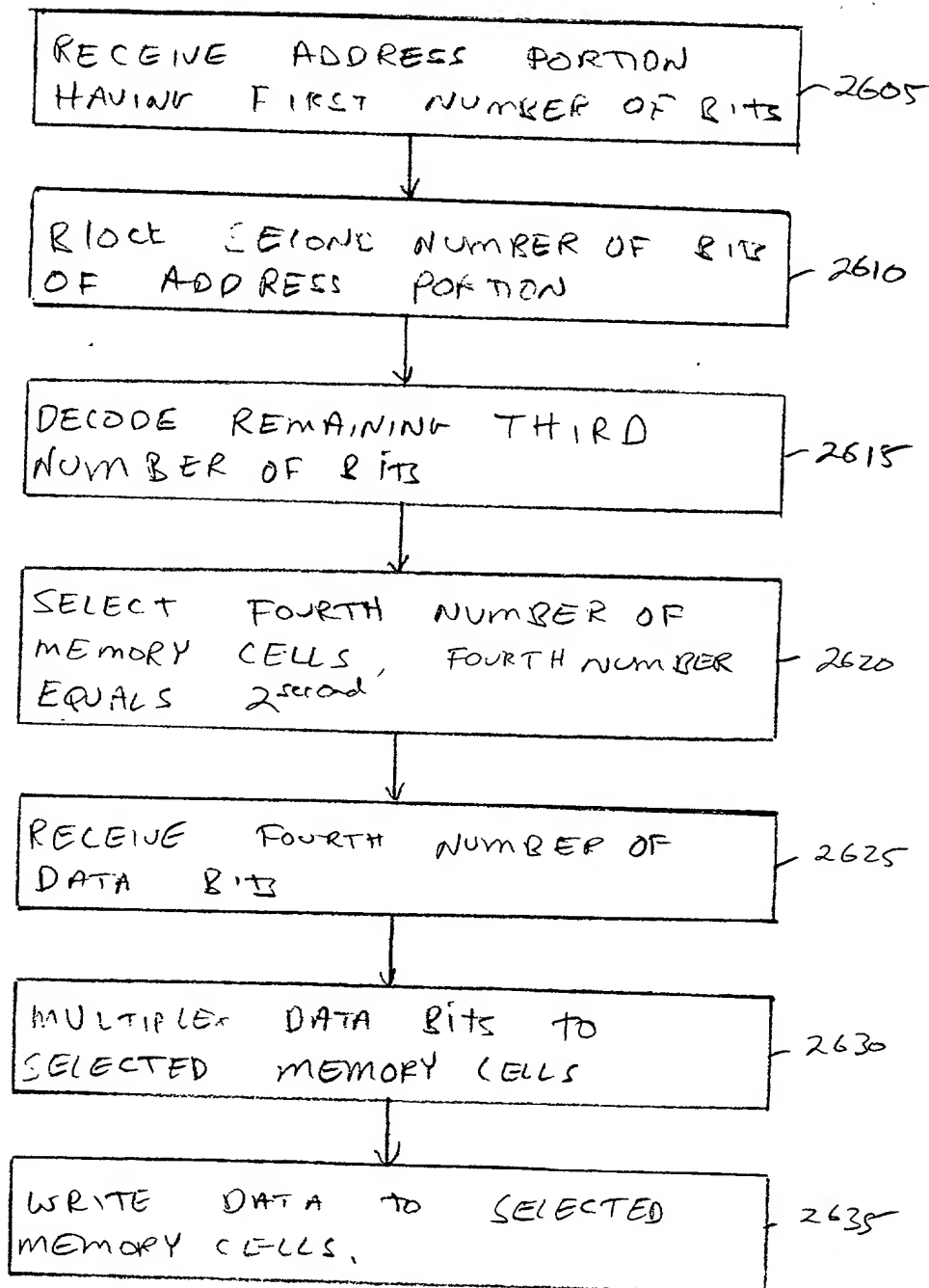


FIGURE 26

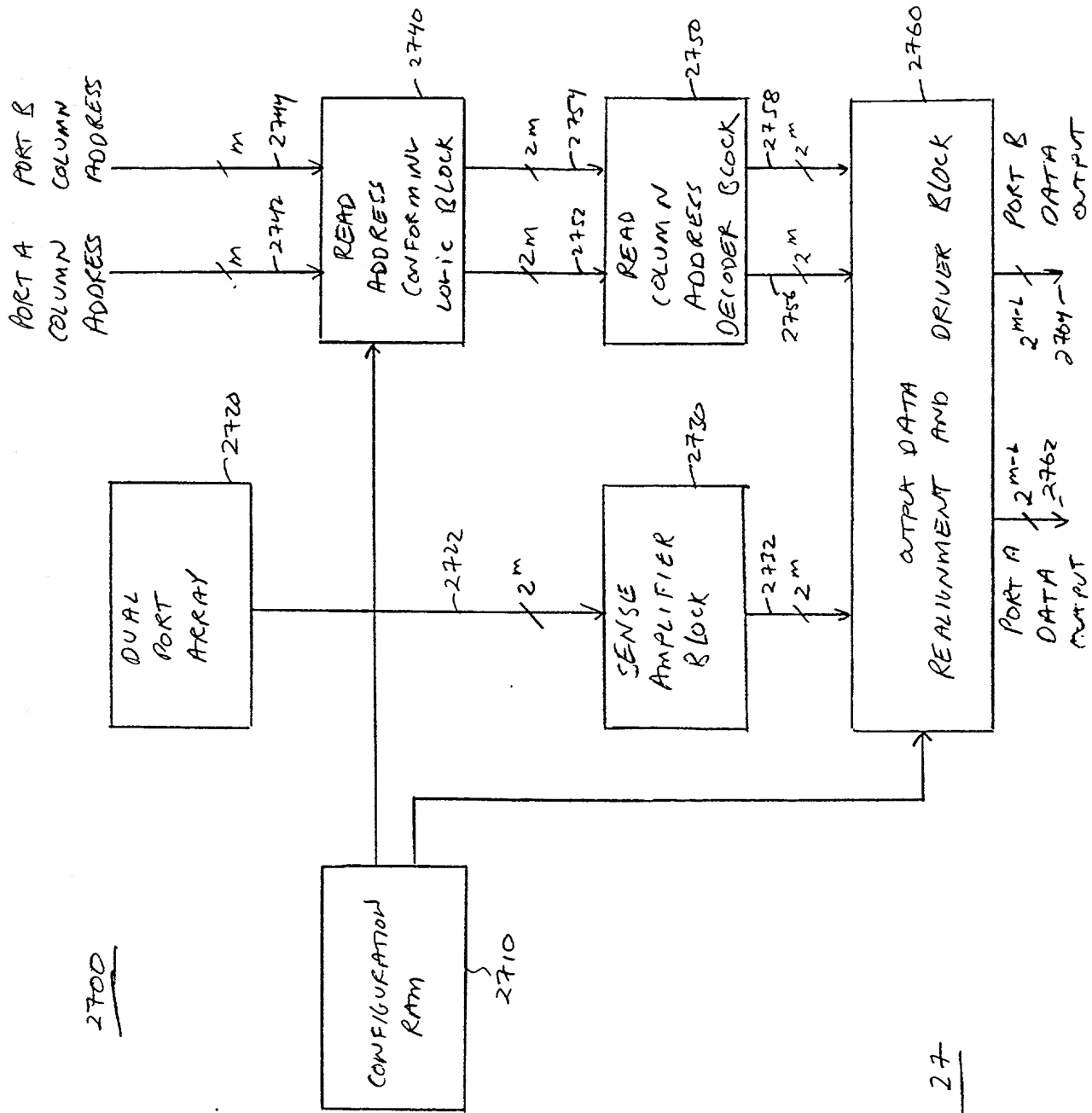


FIGURE 27

28000

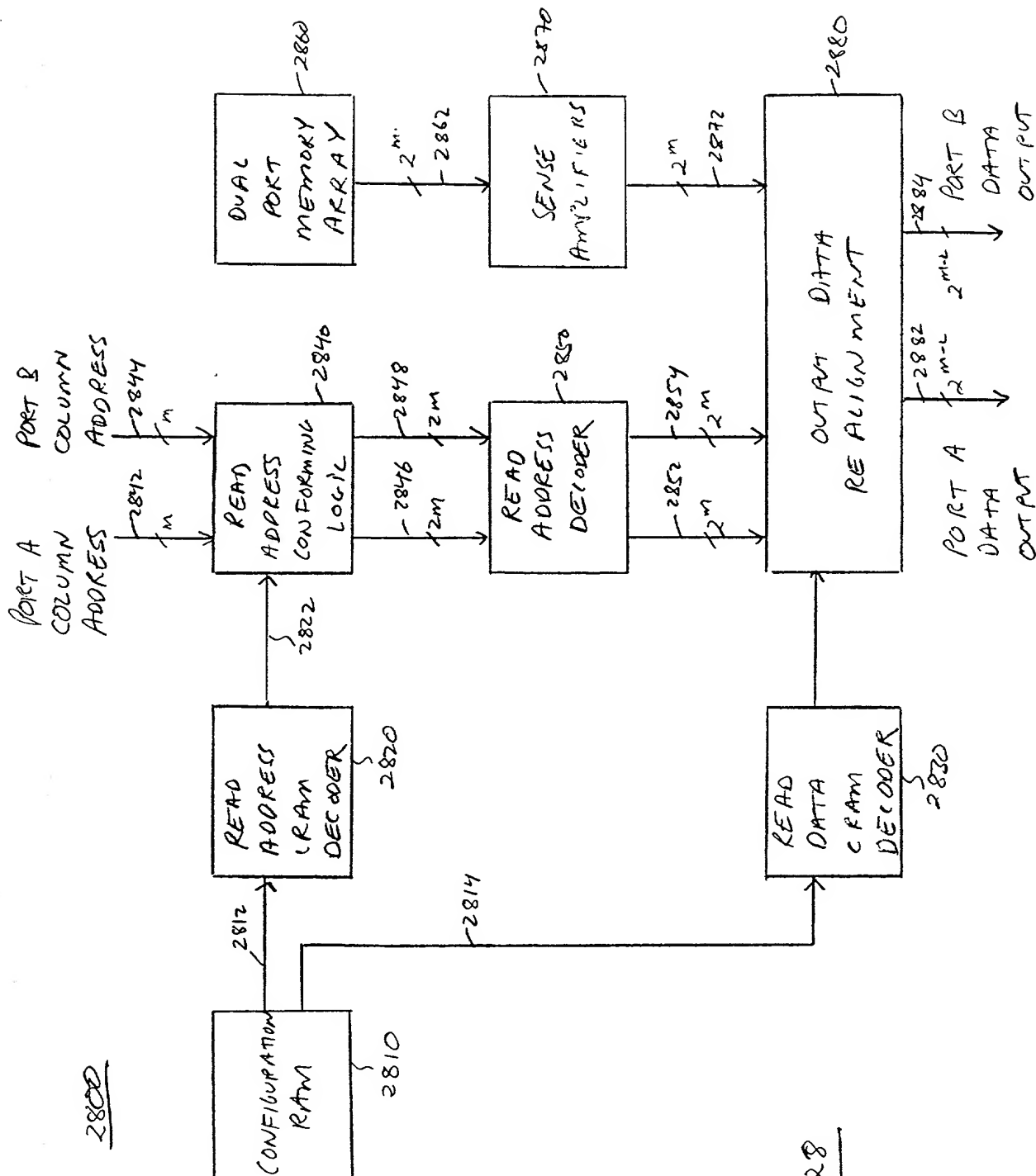
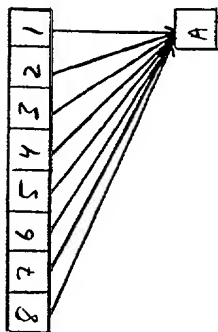
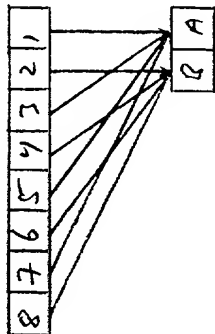


Figure 28

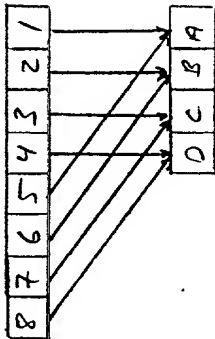
2900



2910



2920



2930

2932 / 2934  
#bits/word

2936 / 2938

MUX	1	2	4	OUTPUTS	TYPE MUX
1	A	A	A	A	1:1
2	A	B	B	A, B	1:2
3	A	A	C	A, C	1:2
4	A	B	D	A, B, D	1:3
5	A	A	A	A	1:1
6	A	B	B	A, B	1:2
7	A	A	C	A, C	1:2
8	A	B	D	A, B, D	1:3

2940

2942 / 2944

2946

MUX	INPUTS	TYPE MUX
A	8, 7, 6, 5, 4, 3, 2, 1	8:1
B	8, 6, 4, 2	4:1
C	7, 3	2:1
D	8	1:1

Figure 29

3000

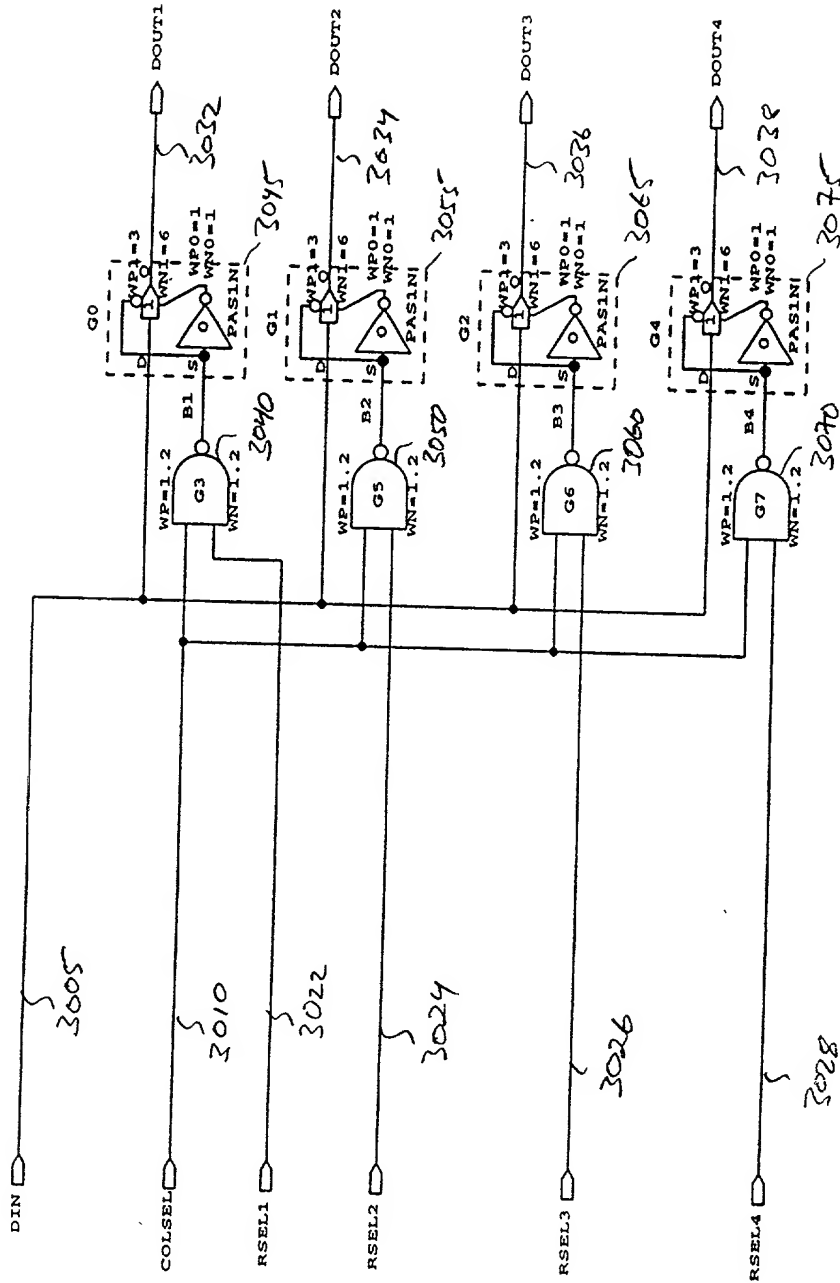


Figure 30

3100

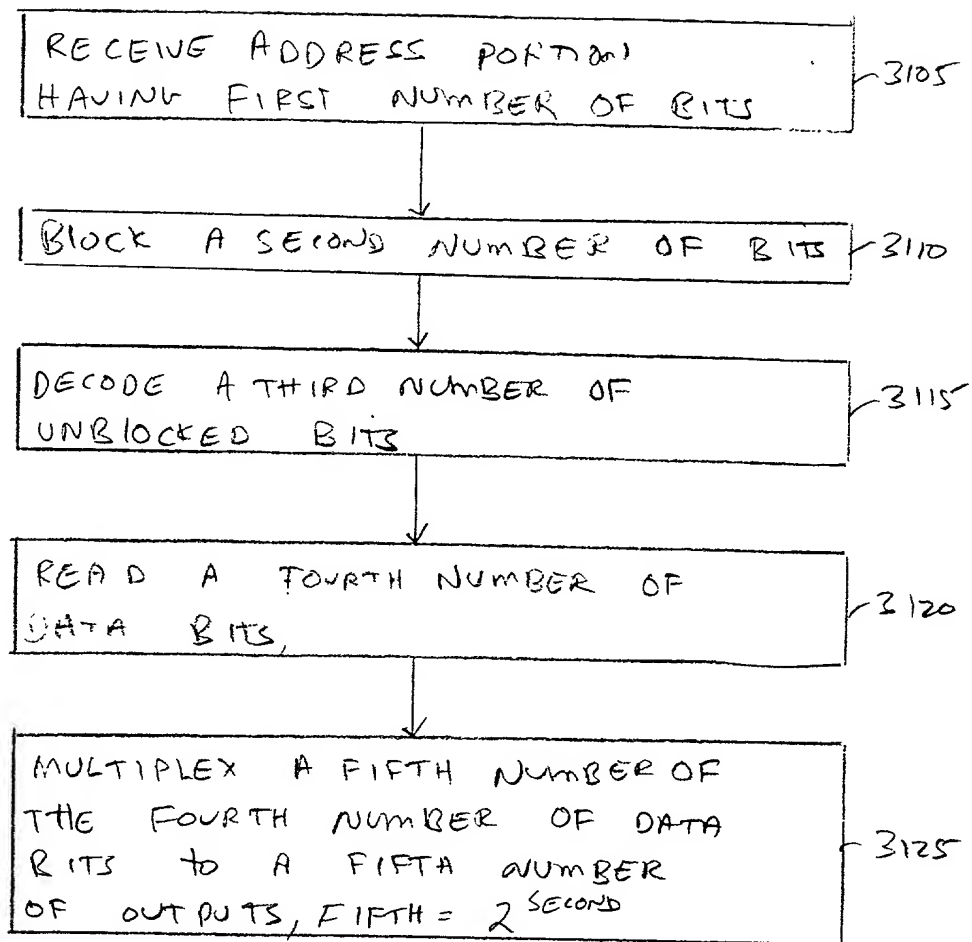


FIGURE 31